

Teaching learning based optimization algorithm for effective analysis of power quality using dynamic voltage restorer

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Article Info

Article history:

Received May 28, 2024

Revised Oct 6, 2024

Accepted Nov 19, 2024

Keywords:

Dynamic voltage restorer

Harmonics estimation

Power quality

Voltage source inverter

Voltage swell

ABSTRACT

In this study, the load voltage is dynamically restored utilising the dynamic voltage restorer (DVR) using the voltage injection approach. The injected voltage is generated using a voltage-source inverter (VSI), which is necessary to correct for the utility network's sag and swell characteristics voltage. The restoration process is dependent on the condition and quality of the utility system, i.e., it injects energy into the external system for the duration of voltage sag, and during voltage swell, energy is absorbed by the compensator from the external system, causing an rise in dc link voltage, which is connected across the VSI. In this study two different controllers are employed based on a learning based optimized algorithm. The simulation results are shown using two different controllers and the performance of the proposed controller is found to be a better one.

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1. INTRODUCTION

Because of the widespread usage of non-linear loads by home and industrial customers, improving power quality (PQ) has become an essential concern in recent years [1]. Voltage quality problems [2], such as voltage sags or swells, are the most common in the network. These voltage-related issues are caused by a variety of sources, including transmission system disturbances, defects in nearby feeders and fuses, and circuit breaker trips. Harmonic components in supply voltages occur as a result of uncompensated nonlinear loads in the utility system. Various gadgets have been developed to address these challenges in order to advance PQ. Conventionally, passive filters [3]-[5] were utilised, however due to various shortcomings [6], they have been replaced by active filters. A series compensator [7]-[9] which is also called as dynamic voltage restorer (DVR) is primarily used to improve PQ by reducing voltage sag and swell conditions. When compared to other custom-power devices in terms of technological advancement and economic value, the DVR is the most effective in utility systems for improving voltage quality [10]-[12]. To improve voltage quality, the DVR is implemented by infusing voltages in series with the three-phase distribution system, resulting in voltage magnitude and phase shift. The negative effects of voltage quality disruptions such as voltage sag and voltage swell are minimised by these devices. During a utility system outage, the DVR can help maintain voltage at a specific level by adding a compensating voltage [13] in series with the voltage across the load.

The demand across the load can be maintained by regulating the power supply from an energy storage device. With the impact of voltage sag, the energy storage device receives power from the utility system via the linked converters. Alternatively, high voltage swell might be harmful to equipment.

Practically, both voltage sag and swell occur concurrently. Furthermore, reactive power imbalances will consequence in overvoltage or swell in the utility network. Extreme and continuous overvoltage may cause harm to the storage device. This paper proposes a novel control mechanism for generating reference current that does not require reactive volt-amperes [14], [15]. To construct a signal, an actual calculation of its amplitude, frequency, and phase is necessary. The current approach is employed to produce the per unit component of the reference waveform for the HSAPF from the harmonic content signal. Compared to other conventional methods, like orthogonal component filter and least square error algorithms, Kalman-filter (KF) is known to be more resistant in noisy situations; thus, in the proposed study, KF is utilised to estimate per unit current reference [16].

The standard proportional-integral-derivative (PID) controller (PID) is less expensive, easy to tune, and but for complex cases, PID is not suitable to supply effective solutions. In such cases, a fuzzy logic controller (FLC) based PID is utilised because it improves performance by updating the PID controller's parameters on a frequent basis [17]-[20]. Again, one of the key benefits of adopting a fuzzy-based PID is that no mathematical formulation is required to select the membership function or the rule base. Generally, certain experiential guidelines are employed to choose the fuzzy constraints, which might not be the best values. As a result of the information presented above, the teaching learning based optimization algorithm (TLBOA) tunes the controller (PID and fuzzy PID) scaling factors to improve controller efficiency [21]-[23]. Therefore, in this study, TLBO-fuzzy PID (TLBOFPID) or TLBO-conventional PID (TLBOPID) is used to extract the reference maximum current as well as to manage the voltage of the inverter's DC link. Various instances are simulated to investigate the compensatory capacities of both TLBOFPID and TLBOPID-based HSAPF [24], [25].

2. SYSTEM ARCHITECTURE

Figure 1 depicts the configuration of the proposed system. A three-phase source is associated to a three-phase non-linear load. The non-linear load is designed to handle both balanced and unbalanced loads. Table 1 contains parameter values for both balanced and unbalanced loads. In this power system configuration, a DVR is fed at the point of common coupling (PCC), where it compensates for the load voltage, reduces harmonics [25], and maintains the DC link voltage. For harmonics estimation, two adaptive control approaches, TLBOPID and TLBOFPID are used.

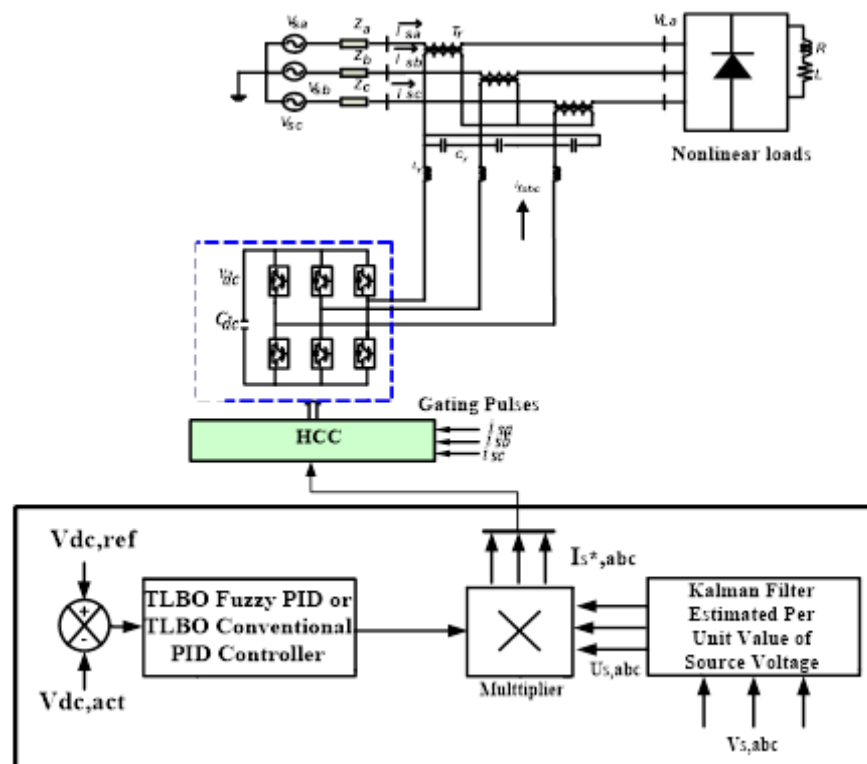


Figure 1. Configuration of the proposed model

Table 1. System-parameters

Parameter	Values
Supply voltage	220 V
Frequency	50 Hz
Supply resistance and inductance	$R_s = 1 \Omega$ and $L_s = 20 \text{ mH}$
Non-linear loads (balanced and unbalanced)	20 Kw $R = 10 \Omega$ and $L = 50 \text{ mH}$
Sampling period	50 μs
Dc link voltage	200 V
Ripple filter	$R_f = 5 \Omega$, $C_f = 10 \mu\text{F}$,
PI gain	$K_p = 0.25$, $K_i = 0.1$

3. CONTROLLER STRATEGY

In this section, the reference current generation using KF and TLBO with PID and FPID are discussed in brief. TLBOA uses a revolutionary knowledge-based technique in which the instructor for the following iteration is chosen from among the previous best students [26]. In order to change the considered pupil's knowledge, the other pupils and their peers must then pick up knowledge from that student and share it with others. The avoidance of early or unexpected convergence and the preservation of multiplicity have made optimisation a popular field of study for many scholars in recent years. The two main stages in evolutionary processes are crossover and mutation, which are not established in TLBOA [27]. Teaching learning behaviour is the driving force behind TLBOA because it shares many characteristics with other evolutionary strategies. Every member of TLBOA has an adjustable search method based on its own learning.

3.1. Initialization

The initial population size i.e., $[n \times m]$ is created in this step, where the numbers of learners are represented by 'n' which is the size of population and the problem dimension is 'm' i.e. the subjects given. In the initial population matrix, the mark obtained by various learners in the j^{th} subject is represented in the j^{th} column.

$$\text{Initial population } X = \begin{bmatrix} x_{1,1} & x_{1,2} & \dots & x_{1,m} \\ x_{2,1} & x_{2,2} & \dots & x_{2,m} \\ \vdots & \vdots & \ddots & \vdots \\ x_{n,1} & x_{n,2} & \dots & x_{n,m} \end{bmatrix} \quad (1)$$

3.2. Teaching phase

The mean result of the class has to be improved by the teacher in the assigned subject in teacher phase. In this article, $X_{j,k,i}$ is represented as the any value of solution where j ($j=1,2,\dots,m$) is the subject taken by the learner, k ($k=1,2,\dots,n$) is the learner itself and i represents the i^{th} iteration. As the learner is taught by the teacher and is assumed to be the best solution $X_{j,k_{\text{best}},i}$ which will be the teacher in the next iteration. The average marks secured by the learner in each subject which is the average value of each column as given below where y_j is the mark obtained by the learner in the j^{th} subject.

$$M_{j,i} = [y_1, y_2, \dots, y_m] \quad (2)$$

The difference between the results obtained by the corresponding teacher with the mean result in that subject taught by the same teacher is written as:

$$\text{Difference_Mean}_{j,k,i} = r_{j,i} (X_{j,k_{\text{best}},i} - T_F M_{j,i}) \quad (3)$$

Where $r_{j,i}$ a random number is vary between 0 to 1 and T_F is the teaching factor taken 1 or 2.

$$T_F = \text{round}[1 + \text{rand}(0,1)] \quad (4)$$

Now, the existing population is to be updated is given as:

$$X'_{j,k,i} = X_{j,k,i} + \text{Difference_Mean}_{j,k,i} \quad (5)$$

The well-run value of $X_{j,k,i}$ is accepted i.e. $X'_{j,k,i}$ only if $f(X'_{j,k,i}) < f(X_{j,k,i})$ else $X_{j,k,i}$ is accepted, where $f(X_{j,k,i})$ is the objective function.

3.3. Learning phase

In this phase, the learner has to improve based on the interactions with other students and if it has more knowledge than him, arbitrarily two learner P and Q are chosen so that $X'_{P,i} \neq X'_{Q,i}$.

$$X''_{j,P,i} = X'_{j,P,i} + r_{j,i}(X'_{j,P,i} - X'_{j,Q,i}) \text{ if } f(X'_{P,i}) < f(X'_{Q,i}) \quad (6)$$

$$X''_{j,P,i} = X'_{j,P,i} + r_{j,i}(X'_{j,Q,i} - X'_{j,P,i}) \text{ if } f(X'_{P,i}) > f(X'_{Q,i}) \quad (7)$$

Allow $X''_{j,P,i}$ if the performance is superior. Based on the above discussion, the flowchart of TLBOA is in Figure 2.

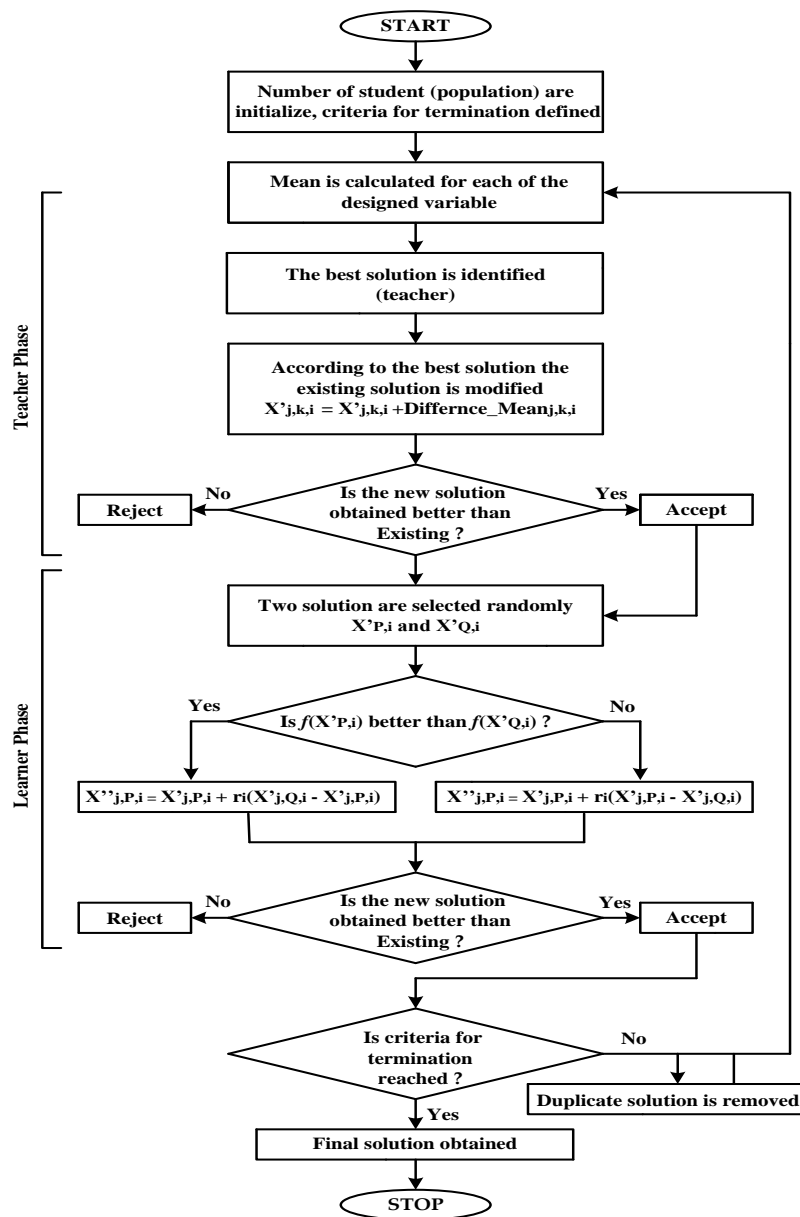


Figure 2. Flowchart of TLBOA

3.4. Objective function

Here, the objective function J of the integral time absolute error (ITAE) type is used. The objective function is thought to be modelled by the DC link capacitor voltage error [7]. The difference between the actual voltage and the DC link reference, or error $e(t)$, is what's being evaluated here, and minimising the error is the primary goal. For this goal function, a sudden increase or drop in load is taken into account.

$$J = \int_0^{t_{sim}} t|e(t)| dt \quad (8)$$

4. RESULTS ANALYSIS

A power system model in MATLAB/Simulink is designed using the DVR, to assess how the suggested controller works in a three-phase system. In this system, the utility source, loads comprising both balanced and unbalanced and the DVR are connected. The injecting of compensating voltage through DVR are done using PID and FPID controllers through a learning based algorithm. Under balanced and unbalanced loads, the suggested TLBOFPID technique and the traditional TLBOPID technique are used to check the performance of the system. Subsequent subsections contain the relevant system parameters. The appendix contains the non-linear loads for balanced and unbalanced loads. When comparing the grid voltage to the typical sinusoidal voltage, distortion is evident. The non-linear load existence causes the grid voltage to exhibit sag and swell voltage characteristics. Figure 3 illustrates the simulated result. The whole voltage profile with sag and swell is displayed in Figure 3(a). Grid voltage with sag lasting 0.5–0.7 seconds is depicted in Figure 3(b), and a swell lasting 1.5–1.7 seconds is shown in Figure 3(c). Distortion in a normal fundamental wave is observed in the load voltage linked to the three-phase utility system. The total harmonic distortion (THD) is determined as 38.97% for unbalanced load. Relative figures are displayed in Figure 3(d). In order to enhance PQ and decrease harmonics, the power system is operated under DVR by utilising the TLBOPID and TLBOFPID harmonics estimation techniques. The performance are analysed under unbalanced load conditioning.

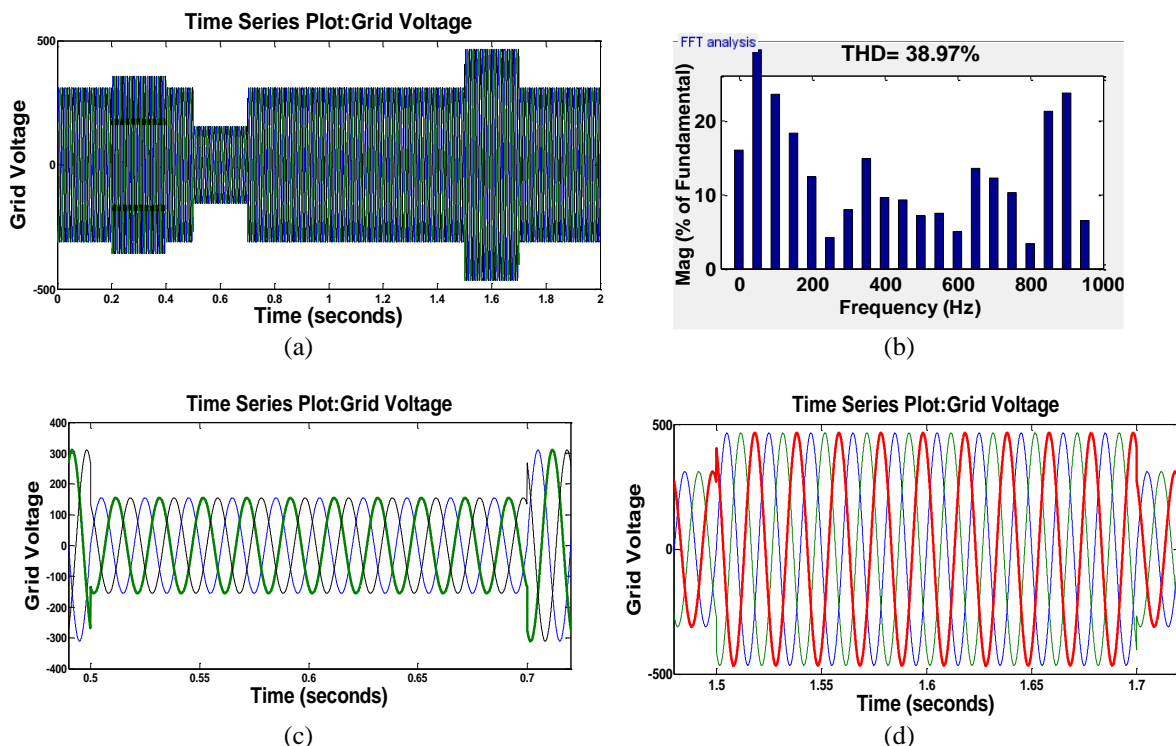


Figure 3. Simulated results of three phase grid system: (a) grid voltage showing sag and swell, (b) THD analysis of load voltage during unbalanced load, (c) grid voltage with voltage sag, and (d) grid voltage with voltage swell

4.1. Unbalanced-loading condition

The DVR under imbalanced non-linear loading conditions is noticed in this subsection. In the first scenario, TLBOPID is used to run the system. Figure 4 displays the same results. Figure 4(a) produces the filter voltage, DC-link voltage, source and load currents. Figure 4(b) expresses the THD value determined to be 4.26%. Additionally, the test is conducted utilising the TLBOFPID methodology. Figure 4(c) displays the results of the single-phase simulation for the source and load current, filter voltage, and DC-link voltage. Figure 4(d) displays the THD value was determined to be 3.72%. Based on the simulation results, it can be observed that the suggested TLBOFPID technique is used with the DVR to adjust for harmonic distortion with non-linear loads. It is observed that the TLBOFPID-based suggested model yields acceptable outcomes. Better DVR performance in these circumstances is implied by the THD of the load voltage.

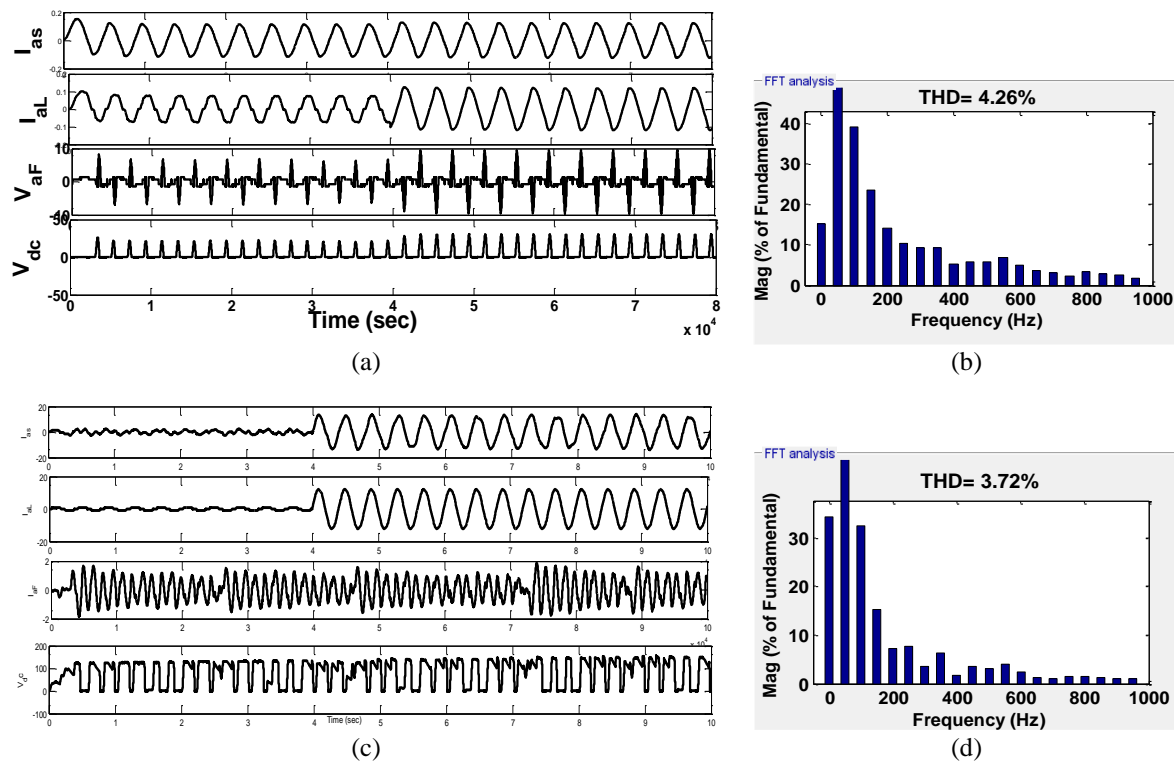


Figure 4. PQ improvement with unbalanced-load applying: (a) TLBOPID, (b) THD value using TLBOPID controller, (c) TLBOFPID, and (d) THD value using TLBOFPID controller

5. CONCLUSION

DVR has been proven to be the most practical, effective tool and commonly used device to improve the voltage profile and PQ of the system. The control circuit and power system model with a sensitive load is designed and simulated using MATLAB/Simulink tool. The proposed design is performed both with and without the DVR. The proposed DVR-based control approach produced a better and smooth voltage profile with very little harmonic content by compensating for the distorted load voltage. The performance of DVR is verified using different two different controllers PID and FPID based on teaching learning optimization techniques. By employing the TLBOPID and TLBOFPID methods to estimate the load voltage and perform DVR control, the error of voltage injection has been minimised. The THD value of TLBOFPID is 3.72% and found to be satisfactory compared to TLBOPID having THD 4.26%. Therefore, on load voltage compensation, the TLBOFPID technique is found to be satisfactory. Further, future scope of DVR can be engaged in different sophisticated semiconductor devices or loads which require harmonics less and reliable power supply using different soft computing approaches.




ACKNOWLEDGEMENTS

This research work was supported by “Woosong University’s Academic Research Funding - 2025”.




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